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IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF :  
Kensaku YAMAGUCHI, et al. : EXAMINER: TURCHEN, JAMES R.  
SERIAL NO: 10/754,571 :  
FILED: JANUARY 12, 2004 : GROUP ART UNIT: 2439  
FOR: DATA ACCESS CONTROL :  
METHOD FOR TAMPER RESISTANT  
MICROPROCESSOR USING CACHE  
MEMORY

AMENDMENT UNDER 37 C.F.R. § 1.116

COMMISSIONER FOR PATENTS  
ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Office Action dated September 17, 2008, please amend the above-identified application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks** begin on page 6 of this paper.